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"ve voltage applied @ its gate (with $V_G < V_S$) reduces current flow from drain to source. It operates with $V_D > V_S$. " In p-channel JFET, a +ve voltage applied @ its gate (with $V_G > V_S$) reduces current flow from source to drain. It operates with $V_S > V_D$. Fri, 09 Nov 2018 10:07:00 GMT Basic Electronics - NYU Tandon School of Engineering - In electronics, potential difference is commonly referred to as voltage, with the symbol V . Sometimes the symbol U or E for emf (electromotive force) is used, but the standard symbol V represents any potential difference. Fri, 09 Nov 2018 19:04:00 GMT Basic Electronics Tutorials - for reasoning about circuits, most of which are not discussed in this paper. Perhaps, the most relevant of these involves geometry and teleology. Presented with a circuit, an engineer will recognize familiar circuit fragments and recall their function. In fact, engineers have explicit geometric conventions for drawing well-known circuit fragments. Wed, 07 Nov 2018 19:56:00 GMT How Circuits Work - de Kleer - PDF | This Book is written for all the people who love innovation. It is the big collection of ideas to do some innovative project, to make something new. Fri, 09 Nov 2018 17:23:00 GMT (PDF) 270 MINI ELECTRONICS PROJECT WITH CIRCUIT

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